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PATENT APPLICATION FEE DETERMINATION RECORD

Substitute for Form PTO-875

Application or Docket Number

10/072346

CLAIMS AS AMENDED - PART II

		(Column 1)	(Column 2)	(Column 3)	SMALL ENTITY		OR	OTHER THAN SMALL ENTITY	
AMENDMENT	10/28/02	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE		RATE	ADDITIONAL FEE
	Total (37 CFR 1.16(c))	37	Minus	20	= 17	X \$	=	X \$ 18	= 306
	Independent (37 CFR 1.16(b))	10	Minus	5	= 5	X \$	=	X \$ 84	= 420
	FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(d))					+	=	+	=
TOTAL ADD'L FEE								TOTAL ADD'L FEE 726 (pd)	
AMENDMENT		CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE		RATE	ADDITIONAL FEE
	Total (37 CFR 1.16(c))	*	Minus	**	=	X \$	=	X \$	=
	Independent (37 CFR 1.16(b))	*	Minus	***	=	X \$	=	X \$	=
	FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(d))					+	=	+	=
TOTAL ADD'L FEE								TOTAL ADD'L FEE	
AMENDMENT		CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE		RATE	ADDITIONAL FEE
	Total (37 CFR 1.16(c))	*	Minus	**	=	X \$	=	X \$	=
	Independent (37 CFR 1.16(b))	*	Minus	***	=	X \$	=	X \$	=
	FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(d))					+	=	+	=
TOTAL ADD'L FEE								TOTAL ADD'L FEE	
AMENDMENT		CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE		RATE	ADDITIONAL FEE
	Total (37 CFR 1.16(c))	*	Minus	**	=	X \$	=	X \$	=
	Independent (37 CFR 1.16(b))	*	Minus	***	=	X \$	=	X \$	=
	FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(d))					+	=	+	=
TOTAL ADD'L FEE								TOTAL ADD'L FEE	

* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.

** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".

*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".

The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.

This collection of information is required by 37 CFR 1.16. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re: Appn. Ser. No. 10/072,346

Art Unit 1623

Filed 2/6/02

Exr. Not Yet Assigned

Inventors Coteus et al

Atty Dkt No. YOR919980423US2

1523
5A
10/4/02



For: ADDRESS WRAP FUNCTION FOR ADDRESSABLE MEMORY DEVICES

PRELIMINARY AMENDMENT

RECEIVED

OCT 30 2002

TECH CENTER 1600/2900

Assistant Commissioner for Patents
U.S. Patent and Trademark Office
Washington, D.C. 20231
Sir:

Kindly preliminarily amend the above identified application as follows:

In the claims:

Kindly add the following additional claims:

- 1 21. In a data processing memory assembly of the type having an input port, an output port, and containing
- 2 information stored in a plurality of addressable storage locations,
- 3 said memory assembly being responsive in processing to data having an address
- 4 information portion and a to be stored portion,
- 5 the improvement comprising
- 6 said processing including a selective capability of directing said address information portion
- 7 of said data appearing in a data path to said plurality of addressable storage locations in a separate
- 8 processing path without disturbing said information stored in said plurality of
- 9 addressable storage locations.

- 1 22. The improvement of claim 21 wherein said selective capability includes
- 2 a separate address information path around said addressable storage locations to said output port, and,
- 3 a processing instruction implementing the directing of said address information portion of said data to said
- 4 separate path.

05/27/2005 SDAVIS 00000003 090468
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01 FC:1201
02 FC:1202
420.00
306.00